

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1114	(timing adj analysis) with circuit	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 09:10
L2	11952	((716/1-21) or (703/13-22)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/07 09:11
L3	664	1 and 2	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 09:11
L4	6	3 and ((determin\$4 or obtain\$4 or calculat\$4) with output with timing) and (select\$4 with input with timing) and (worst with case)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/07 10:12

L5	4	4 and (gate with delay\$2) and (select\$4 with delay\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:36
L7	3	5 and ((determin\$4 or obtain\$4 or calculat\$4) with gate with delay\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:19
L8	2	7 and tlf	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:28
L9	166	3 and (tlf or (timing with library))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:29
L10	4	9 and ((timing with model) same (delay with gate) same (timing with output\$2))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:34

L11	1	4 and ((timing with model) same (delay with gate) same (timing with output\$2))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:35
L12	5	4 and library	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:40
L13	4	10 and (slew with output) and (input with slew)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:41
L14	2	10 and (slew with output) and (input with slew) and (gate adj delay)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/02/07 10:49
L16	260	(703/19).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/02/07 10:49